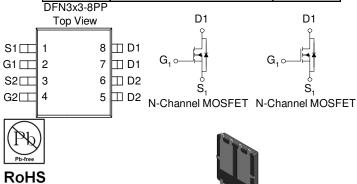
Dual N-Channel 40-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

•	Low r _{DS(on)} provides higher efficiency and
	extends battery life

- Low thermal impedance copper leadframe DFN3x3-8PP saves board space
- Fast switching speed
- High performance trench technology

PRODUCT SUMMARY					
$V_{DS}(V)$	$r_{DS(on)} m(\Omega)$	$I_{D}(A)$			
40	$35 @ V_{GS} = 10V$	7.0			
40	$46 @ V_{GS} = 4.5V$	6.1			



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NO					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage	V_{DS}	40	V		
Gate-Source Voltage	V_{GS}	20	V		
	$T_A=25^{\circ}C$	т	7		
Continuous Drain Current ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	1_{D}	5.7	A	
Pulsed Drain Current ^b	I_{DM}	±20			
Continuous Source Current (Diode Conduct	I_S	7	A		
D a	$T_A=25^{\circ}C$	D	2.5	W	
Power Dissipation ^a	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	P_{D}	1.3	VV	
Operating Junction and Storage Temperatur	T_J, T_{stg}	-55 to 150	°C		

COMPLIANT HALOGEN FREE

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
a	t <= 10 sec	$R_{ heta JA}$	50	00000	
Maximum Junction-to-Ambient ^a	Steady State	$R_{ heta JC}$	7	°C/W	

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Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Analog Power AM7640N

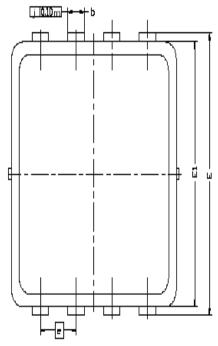
SPECIFICATIONS (T _A = 25°C UNLESS OTHERWISE NOTED)						
D			Limits			TT •4
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static						
Gate-Threshold Voltage	$V_{GS(th)}$	VGS = VDS, $ID = 250 uA$	1			V
Gate-Body Leakage	I_{GSS}	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 32 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA
On-State Drain Current ^A	$I_{D(on)}$	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			A
Drain-Source On-Resistance ^A	r	VGS = 10 V, ID = 1 A			35	mΩ
Drain-Source On-Resistance	r _{DS(on)}	VGS = 4.5 V, ID = 1 A			46	11132
Forward Tranconductance ^A	g_{fs}	$V_{DS} = 15 \text{ V}, I_D = 1 \text{ A}$		26		S
Dynamic			-		-	•
Total Gate Charge	Q_{g}	N-Channel		7		
Gate-Source Charge	Q_{gs}	V_{DS} =15V, V_{GS} =4.5V, I_D =1A		2		nC
Gate-Drain Charge	Q_{gd}	ν _{DS} =13 ν, ν _{GS} =4.3 ν, 1 _D =1/1		2		
Input Capacitance	C_{iss}	N-Channel		400		
Output Capacitance	C_{oss}	V_{DS} =15V, V_{GS} =0V, f=1MHz		80		pF
Reverse Transfer Capacitance	C_{rss}	VDS-13 V, VGS-0 V, 1-11V1112		40		
Turn-On Delay Time	$t_{d(on)}$	N-Chaneel		2		
Rise Time	t _r	V_{DD} =15V, VGS=10V, ID=1A,		3		nS
Turn-Off Delay Time	$t_{d(off)}$	R_{GEN} =25 Ω		12		
Fall-Time	$t_{ m f}$	NGEN-2322		4		

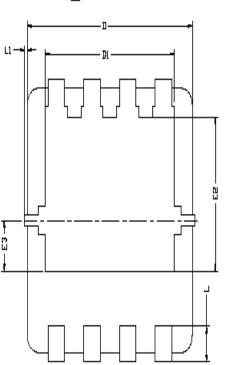
Notes

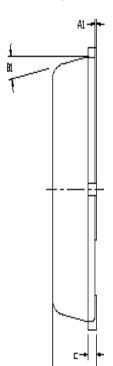
- a. Pulse test: $PW \le 300$ us duty cycle $\le 2\%$.
- b. Guaranteed by design, not subject to production testing.

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Package Information







DIM.	HOLLIMETERS			CNCHES			
ושזעו	MIN	NON	MAX	MIN	NON	MAX	
h	0,700	0,87	0,900	0.0276	0,0315	M354	
AL	ן,ס		1,05	ססס,		0.005	
Ь	0.24	0.30	0,35	0.009	0.012	0.014	
Г	0.10	0.152	25	0.004	0.006	0,010	
	, .	1.00 BO	n	0.11B BSC			
	a C	.35 BS	л u	028 Ee0.0			
		20 BS	ր v]	126 BS	iÇ	
EL	3.00 BSC			(.118 BS	Ç	
E5	1.75 BSC			0.069 BSC			
E3	0.575 BSC			0.023 BSC			
P	0.65 BSC			0.026 BSC			
	0.30	0,40	150	0,0118	0,0157	0.0197	
)		0.100	0		0.004	
81	D,	10,	12"),	10,	121	